# Model Numbering Guide - VCXO

## Available options

| Type | Package (mm) | Supply Voltage (V) | Tri-State Function | Freq. Stability / APR (ppm) | Temp. Range (°C) | Output Logic And Symmetry | Oscillator Mode | Appearance | Lead Free | Dash | Freq. (MHz) |
|---|---|---|---|---|---|---|---|---|---|---|---|
| V: VCXO | T: 7.0x5.0 (6 pad)<br>W:5.0x3.2<br>K: 14.3x8.7 | C: 5<br>E: 3.3<br>J: 2.5<br>K:1.8 | U: Relative Pulling (Refer to Center Voltage) with Tri-State to pin 2<br>M: Multiplier Frequency with Tri-State to pin 2<br>S: Enable Low<br>R: Input to pin 5<br>F: Without Tri-State | M: ±25/±50 (VC=10%Vdd~90%Vdd)<br>P: ±50/±50 (VC=10%Vdd~90%Vdd)<br>A: ±50/±50 (VC=0V~Vdd)<br>B: ±25/±50 (VC=0V~Vdd) | I:-10 ~ +60<br>C:-20 ~ +70<br>L:-40 ~ +85 | J: CMOS 15pF / 50±5%<br>F: CMOS 50pF / 50±5%<br>L: PECL / 50±5%<br>V: LVDS / 50±5% | A:AT Fundamental<br>Not Selectable by Customer | N: Normal | F:RoHS Compliant | | XX.XXXXXX |
| | C:7.5x5.0 (4 pad) | C: 5<br>E: 3.3 | U: Enable High<br>M: Multiplier Frequency | | I:-10 ~ +60<br>C:-20 ~ +70<br>L:-40 ~ +85 | J: CMOS 15pF / 50±5%<br>F: CMOS 50pF / 50±5% | | | | | |

**V T E S P C L A N F - 10.000000**

*Not all combinations of options are available.

### Example: VTESPCLANF-10

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|---|---|
| **Type** | VCXO |
| **Package** | 7.0 x 5.0 mm |
| **Supply Voltage(V)** | 3.3 V |
| **Tri-State** | Enable Low |
| **Freq. Stability ±50 ppm** | Pulling Range ±100ppm |
| **Temp Range** | -20~+70 °C |
| **PECL Output** | Symmetry 50±5% |
| **Oscillator Mode** | AT Fundamental |
| **Appearance** | Normal Appearance |
| **Lead Free** | RoHS Compliant |
| **Frequency** | 10.000000 MHz |